

1. General Description

The EM74HC132; EM74HCT132 is a quad 2-input NAND gate with Schmitt-trigger inputs. Inputs include clamp diodes. This enables the use of current limiting resistors to interface inputs to voltages in excess of V_{CC} . Schmitt trigger inputs transform slowly changing input signals into sharply defined jitter-free output signals.

2. Features and Benefits

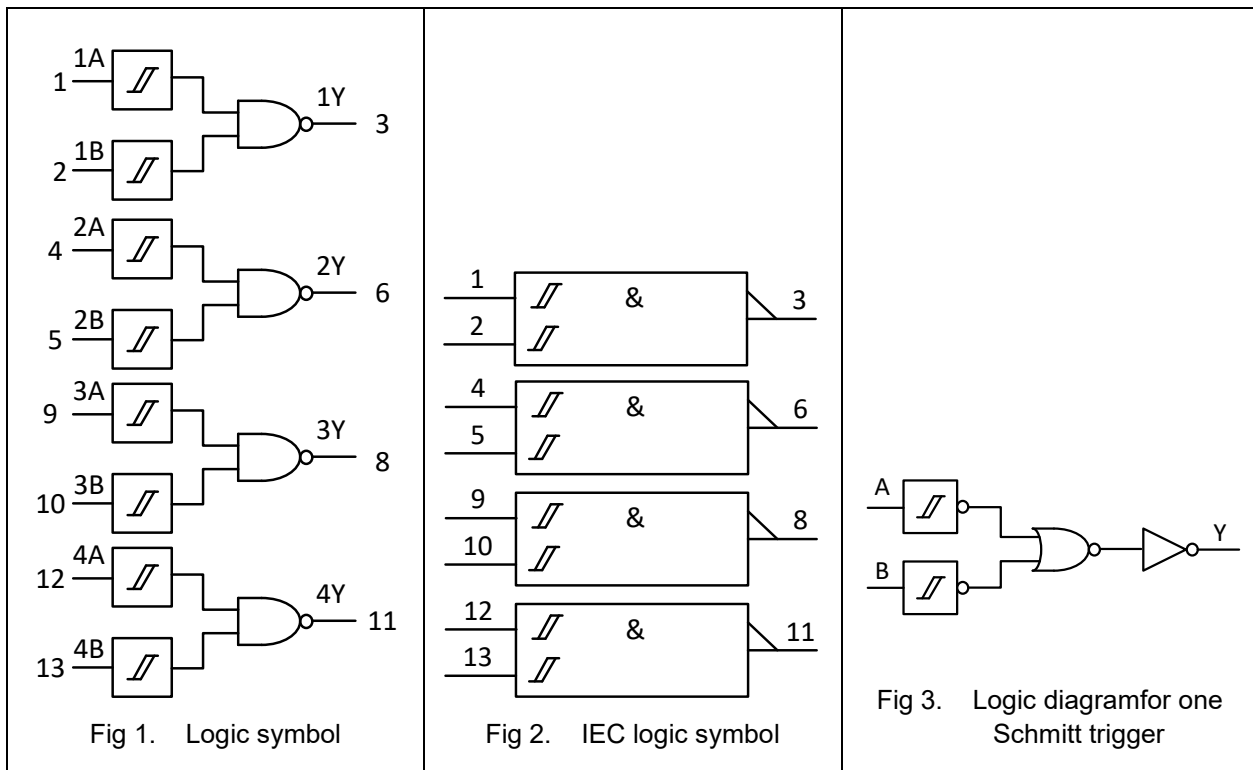
- Wide supply voltage range from 2.0 V to 6.0 V
- CMOS low power dissipation
- High noise immunity
- Latch-up performance exceeds 250 mA
- Unlimited rise and fall times
- Complies with JEDEC standard
 - JESD8C (2.7 V to 3.6 V)
 - JESD7A (2.0 V to 6.0 V)
- ESD protection:
 - HBM ANSI/ESDA/JEDEC JS-001 Class 3A exceeds 4500 V
 - CDM ANSI/ESDA/JEDEC JS-002 Class C3 exceeds 1000 V
- Multiple package options

3. Ordering Information

Table 1. Ordering information

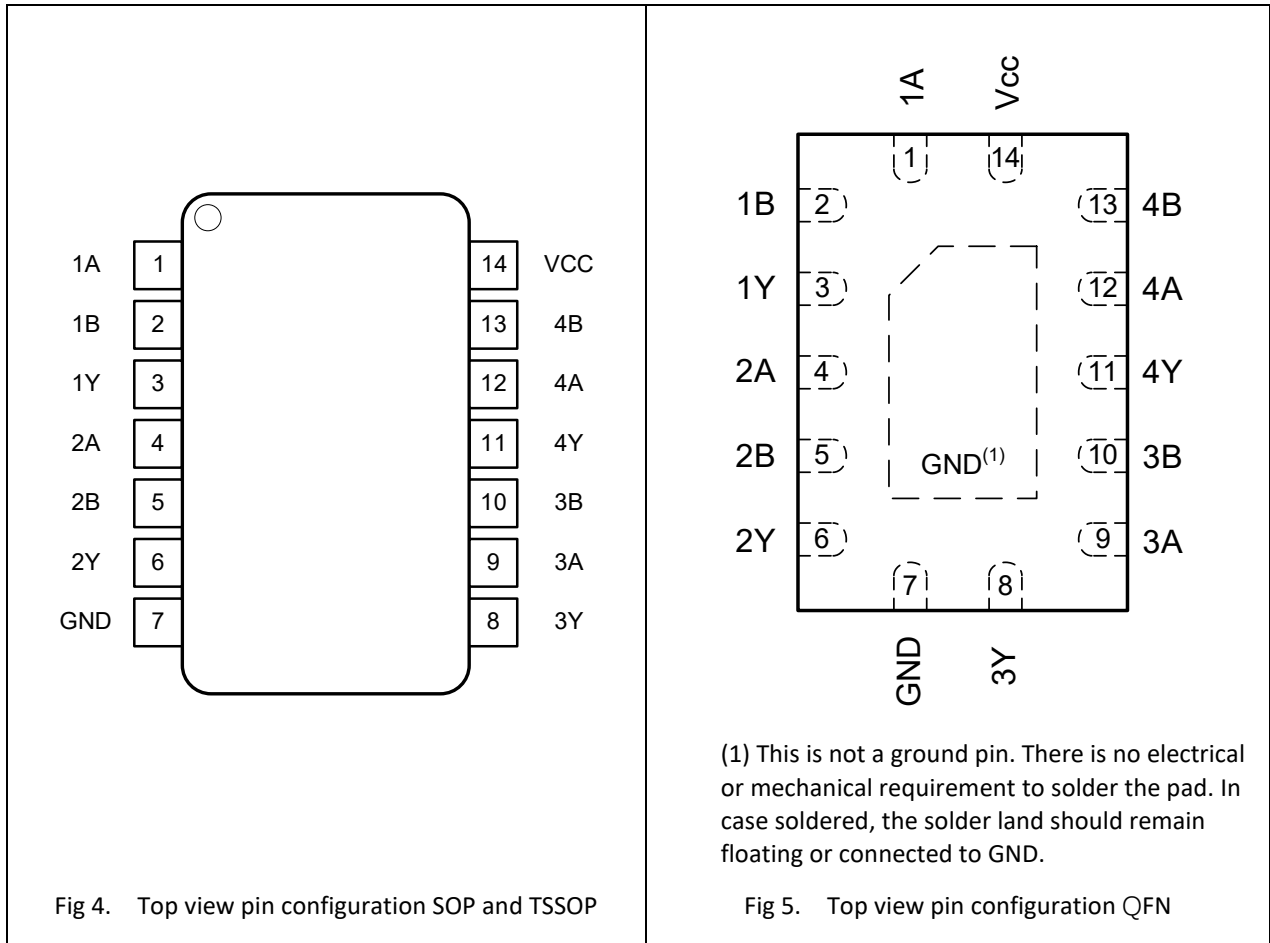
Type number	Topside marking	Package		Quantity
		Name	Description	
EM74HC132D	HC132	SOP-14L	plastic small outline package; 14 leads; body width 3.9 mm	3000
EM74HCT132D	HCT132			
EM74HC132PW	HC132	TSSOP14L	plastic thin shrink small outline package; 14 leads; body width 4.4 mm	3000
EM74HCT132PW	HCT132			
EM74HC132BQ	HC132	QFN2.5x3-14L	plastic dual in-line compatible thermal enhanced very thin quad flat package; no leads; 14 terminals; body 2.5 × 3 × 0.85 mm	3000
EM74HCT132BQ	HCT132			

4. Function Diagram



5. Pinning Information

5.1. Pin map



5.2. Pin description

Table 2. Pin description

Symbol	Pin	Description
1A, 2A, 3A, 4A	1, 4, 9, 12	Data input
1B, 2B, 3B, 4B	2, 5, 10, 13	Data input
1Y, 2Y, 3Y, 4Y	3, 6, 8, 11	Data output
GND	7	Ground (0V)
VCC	14	Supply voltage