

## 1. General Description

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The EM74HC365; EM74HCT365 is a hex buffer/line driver with 3-state outputs controlled by the output enable inputs ( $\overline{OEn}$ ). A HIGH on  $\overline{OEn}$  causes the outputs to assume a high impedance OFF-state. Inputs include clamp diodes. This enables the use of current limiting resistors to interface inputs to voltages in excess of  $V_{CC}$ .

## 2. Features and Benefits

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- Wide supply voltage range from 2.0 V to 6.0 V
- High noise immunity
- CMOS low power dissipation
- Latch-up performance exceeds 250 mA
- Complies with JEDEC standards:
  - JESD8C (2.7 V to 3.6 V)
  - JESD7A (2.0 V to 6.0 V)
- Input levels:
  - For EM74HC365: CMOS level
  - For EM74HCT365: TTL level
- ESD protection:
  - HBM ANSI/ESDA/JEDEC JS-001 Class 2 exceeds 3500 V
  - CDM ANSI/ESDA/JEDEC JS-002 Class C3 exceeds 2000 V
- Multiple package options

## 3. Ordering Information

**Table 1. Ordering information**

Type number	Package		
	Name	Description	Quantity
EM74HC365D	SOP-16L	plastic small outline package; 16 leads; body width 3.9 mm	3000
EM74HCT365D			
EM74HC365PW	TSSOP-16L	plastic thin shrink small outline package; 16 leads; body width 4.4 mm	3000
EM74HCT365PW			

## 4. Function Diagram

