

1. General Description

The EM74AHC1G00; EM74AHCT1G00 is a single 2-input NAND gate. Inputs are overvoltage tolerant.

This feature allows the use of these devices as translators in mixed voltage environments.

2. Features and Benefits

- Wide supply voltage range from 2.0 V to 5.5 V
- Overvoltage tolerant inputs to 5.5 V
- High noise immunity
- CMOS low power dissipation
- Latch-up performance exceeds 100 mA
- Symmetrical output impedance
- Balanced propagation delays
- Input levels:
 - For EM74AHC1G00: CMOS level
 - For EM74AHCT1G00: TTL level
- ESD protection:
 - HBM ANSI/ESDA/JEDEC JS-001 Class 3B exceeds 8000 V
 - MM JESD22-A115C Class C exceeds 550 V
 - CDM ANSI/ESDA/JEDEC JS-002 Class C3 exceeds 2000 V
- Multiple package options

EM74AHC1G00; EM74AHCT1G00

Single 2-input NAND gate

3. Ordering Information

Table 1. Ordering information

Type number	Topside marking	Package		Quantity
		Name	Description	
EM74AHC1G00GV	A1YW	SOT23-5L	SOT23 package, 5 pins 2.92 mm × 1.6 mm; 1.25 mm (Max) height	3000
EM74AHCT1G00GV	C1YW			
EM74AHC1G00GW	A1YW	SOT353	SOT353 package, 5 pins 2.1 mm × 1.25 mm; 1.1 mm (Max) height	3000
EM74AHCT1G00GW	C1YW			
EM74AHC1G00DRL	A1YW	SOT553	SOT553 package, 5 pins 1.6 mm × 1.2 mm; 0.6 mm (Max) height	3000
EM74AHCT1G00DRL	C1YW			

4. Function Diagram

