

1. General Description

The EM74AHC2G04; EM74AHCT2G04 is a dual inverter. Inputs are overvoltage tolerant. This feature allows the use of these devices as translators in mixed voltage environments.

2. Features and Benefits

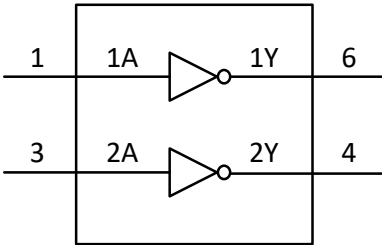
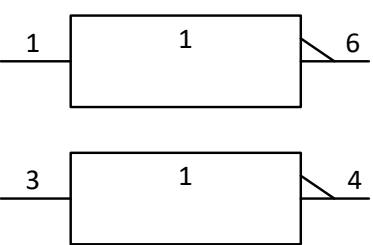
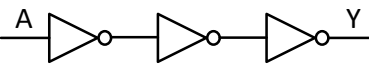
- Wide supply voltage range from 2.0 V to 5.5 V
- Overvoltage tolerant inputs to 5.5 V
- High noise immunity
- CMOS low power dissipation
- Latch-up performance exceeds 200 mA
- Symmetrical output impedance
- Balanced propagation delays
- Input levels:
 - For EM74AHC2G04: CMOS level
 - For EM74AHCT2G04: TTL level
- ESD protection:
 - HBM ANSI/ESDA/JEDEC JS-001 Class 3A exceeds 7000 V
 - CDM ANSI/ESDA/JEDEC JS-002 Class C3 exceeds 2000 V
- Multiple package options

3. Ordering Information

Table 1. Ordering information

Type number	Topside marking	Package		
		Name	Description	Quantity
EM74AHC2G04GV	AYYW	SOT23-6L	SOT23 package, 6 pins 2.92 mm × 1.6 mm; 1.25 mm (Max) height	3000
EM74AHCT2G04GV	CYYW			
EM74AHC2G04GW	AYYW	SOT363	SOT363 package, 6 pins 2.1 mm × 1.25 mm; 1.1 mm (Max) height	3000
EM74AHCT2G04GW	CYYW			
EM74AHC2G04DRL	AYYW	SOT563	SOT563 package, 6 pins 1.6 mm × 1.2 mm; 0.6 mm (Max) height	3000
EM74AHCT2G04DRL	CYYW			

4. Function Diagram

 Fig. 1. Logic symbol	 Fig. 2. IEC logic symbol	 Fig. 3. Logic diagram(one gate)
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5. Pinning Information

5.1. Pinning

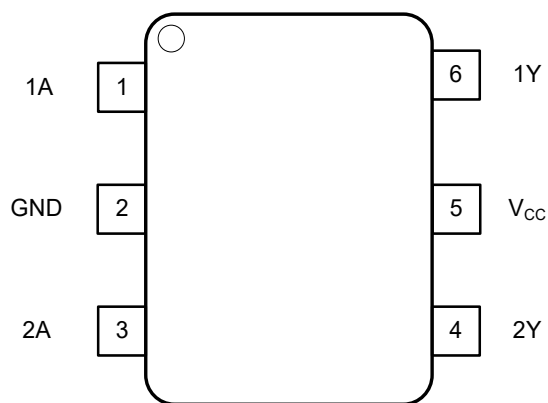


Fig. 4. Top view pin configuration SOT23-6, SOT363 and SOT563

5.2. Pin description

Table 2. Pin description

Symbol	Pin	Description
1A	1	Data input
GND	2	Ground (0V)
2A	3	Data input
2Y	4	Data output
V _{cc}	5	Supply voltage
1Y	6	Data output

6. Functional Description

Table 3. Function table

H = HIGH voltage level; L = LOW voltage level.

Input	Output
nA	nY
L	H
H	L