

1. General Description

The EM74CBT3257 is a high performance, quad, Single Pole Double Throw (SPDT) analog switch that features ultra-low R_{on} of $1.9\ \Omega$ (typical) at $5.5V\ V_{CC}$. The EM74CBT3257 operates over a wide V_{CC} range of $1.5V$ to $5.5V$ and is designed for break-before-make operation. The select input is TTL-level compatible.

EM74CBT3257 is also featured with smart circuitry to minimize V_{CC} leakage current even when the control voltage is lower than V_{CC} supply voltage. This feature suits mobile handset applications by allowing direct interface with baseband processor general-purpose IO with minimal battery consumption. In other word, there is no need of additional device to shift control level to be the same as that of V_{CC} in real application.

The EM74CBT3257 is available in SOP-16L and TSSOP-16L package. Standard Products are Pb-free and halogen-free.

2. Features and Benefits

- Wide supply voltage range from $1.5\ V$ to $5.5\ V$
- Low On-resistance, $R_{ON} = 1.9\ \Omega$ when $V_{CC} = 5.5\ V$
- High Bandwidth: $-3\ dB$ @ $700\ MHz$
- Rail-to-Rail Signal Range
- Break-Before-Make Switching
- Low quiescent current over an Expanded Control Input Range
- ESD protection: HBM ESDA/JEDEC JS-001 Class 2 exceeds $2000\ V$

3. Applications

- Cell phones, PDA, Digital Camera and Notebook
- LCD Monitor, TV and Set-Top Box
- Audio and Video Signal Routing
- Other electronic equipment

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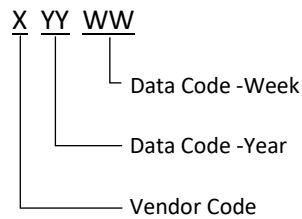
4. Ordering Information

Table 1. Ordering information

Type number	Topside marking	Package		
		Name	Description	Quantity
EM74CBT3257D	CBT3257 YYYWW	SOP-16L	plastic small outline package; 16 leads; body width 3.9 mm	3000
EM74CBT3257PW	CBT3257 YYYWW	TSSOP-16L	plastic thin shrink small outline package; 16 leads; body width 4.4 mm	3000

MARKING INFORMATION

NOTE: YYYWW = Vendor Code and Data Code.



5. Function Diagram

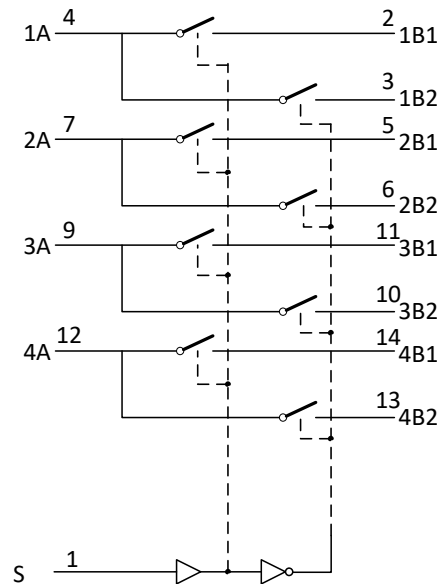
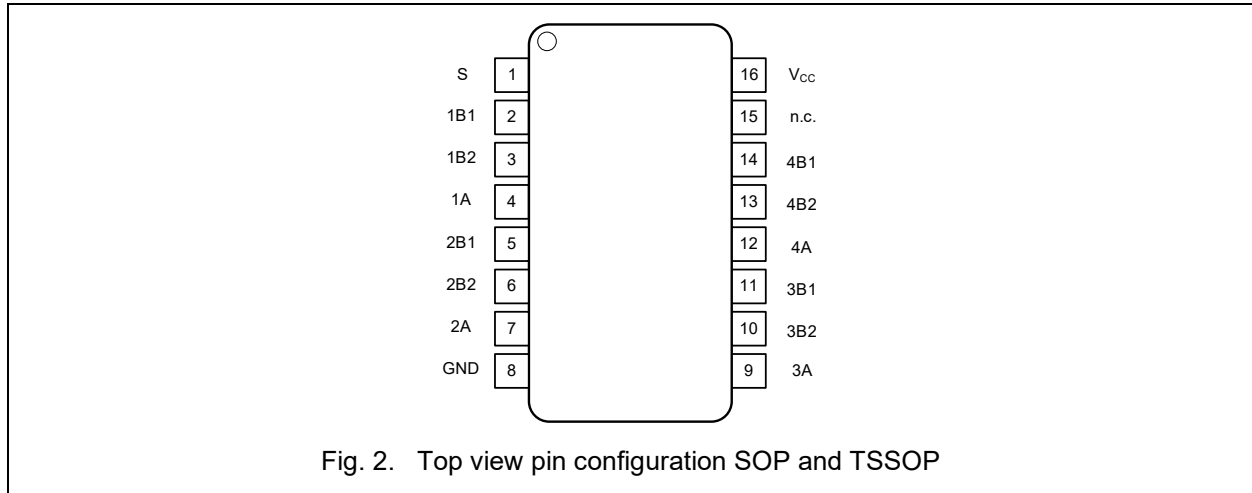


Fig. 1. Logic diagram

6. Pinning Information

6.1. Pinning



6.2. Pin description

Table 2. Pin description

Symbol	Pin	Description
S	1	Select input
1B1 to 4B1	2, 5, 11, 14	B1 input/output
1B2 to 4B2	3, 6, 10, 13	B2 input/output
1A to 4A	4, 7, 9, 12	A input/output
GND	8	Ground (0 V)
n.c.	15	Not connected
V _{CC}	16	Supply voltage

7. Functional Description

Table 3. Function table

H = HIGH voltage level; L = LOW voltage level; X = don't care.

Inputs	Function switch
S	
L	nA = nB1
H	nA = nB2

8. Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Table 4. Absolute Maximum Ratings

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND.

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	supply voltage		-0.3	6.5	V
V _{IN}	control Input Voltage		-0.3	6.5	V
V _A	signal input voltage		-0.3	6.5	V
I _A	continuous Current Through COM, NC, NO			±100	mA
	peak Current Through COM, NC, NO (pulsed at 1ms 50% duty cycle)			±200	mA
T _J	junction Temperature under Bias			150	°C
T _L	lead Temperature (Soldering, 10 seconds)			260	°C
R _{θJA}	thermal resistance			350	°C/W

9. Recommended Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. EnergyMath does not recommend exceeding them or designing to Absolute Maximum Ratings.

Table 5. Recommended Operating Conditions

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	supply voltage		1.5	5.5	V
V _{IN}	control input voltage		0	5.5	V
V _A	signal input voltage		0	V _{CC}	V
T _{amb}	ambient temperature		-40	125	°C

10. Electrical Characteristics

Table 6. Static characteristics
 $T_{amb}=25\text{ }^{\circ}\text{C}$, $V_{CC}=3.3\text{ V}$, unless otherwise specified

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
DC Electrical Characteristics						
V_{IH}	input logic high level	$V_{CC} = 3.4\text{ V to }5.5\text{ V}$	1.6			V
		$V_{CC} = 1.5\text{ V to }3.3\text{ V}$	1.4			V
V_{IL}	input logic low level	$V_{CC} = 3.4\text{ V to }5.5\text{ V}$			0.6	V
		$V_{CC} = 1.5\text{ V to }3.3\text{ V}$			0.4	V
I_{CC}	supply current	$I_A = 0$, $V_{SEL} = 0$ or $V_{SEL} = V_{CC}$			1.0	μA
ΔI_{CC}	additional supply current	$I_A = 0$, $V_{CC} = 2.5\text{ V or }4.2\text{ V}$ $V_{SEL} = 1.6\text{ V}$			3.0	μA
I_{off}	OFF-state switch leakage current from COM to NC (or NO)	$V_A = 5\text{ V}$, V_{NC} or $V_{NO} = 0\text{ V}$			± 2.0	μA
R_{ON}	on-resistance	$V_{CC} = 3.5\text{ V}$, $V_A = 0.8\text{ V to }2.0\text{ V}$, $I_A = 10\text{ mA}$		3.4	4.0	Ω
		$V_{CC} = 4.5\text{ V}$, $V_A = 0.8\text{ V to }2.3\text{ V}$, $I_A = 10\text{ mA}$		2.4	3.0	Ω
		$V_{CC} = 5.5\text{ V}$, $V_A = 0.8\text{ V to }2.6\text{ V}$, $I_A = 10\text{ mA}$		1.9	2.5	Ω
R_{FLAT}	on-resistance flatness	$V_{CC} = 3.5\text{ V}$, $V_A = 0.8\text{ V to }2.0\text{ V}$, $I_A = 10\text{ mA}$		0.2	0.4	Ω
		$V_{CC} = 4.5\text{ V}$, $V_A = 0.8\text{ V to }2.3\text{ V}$, $I_A = 10\text{ mA}$		0.25	0.45	Ω
		$V_{CC} = 5.5\text{ V}$, $V_A = 0.8\text{ V to }2.6\text{ V}$, $I_A = 10\text{ mA}$		0.3	0.5	Ω
ΔR_{ON}	on-resistance matching between channels	$V_{CC} = 3.5\text{ V}$, $V_A = 0.8\text{ V to }2.0\text{ V}$, $I_A = 10\text{ mA}$		0.1	0.2	Ω
		$V_{CC} = 4.5\text{ V}$, $V_A = 0.8\text{ V to }2.3\text{ V}$, $I_A = 10\text{ mA}$		0.1	0.2	Ω
		$V_{CC} = 5.5\text{ V}$, $V_A = 0.8\text{ V to }2.6\text{ V}$, $I_A = 10\text{ mA}$		0.1	0.2	Ω

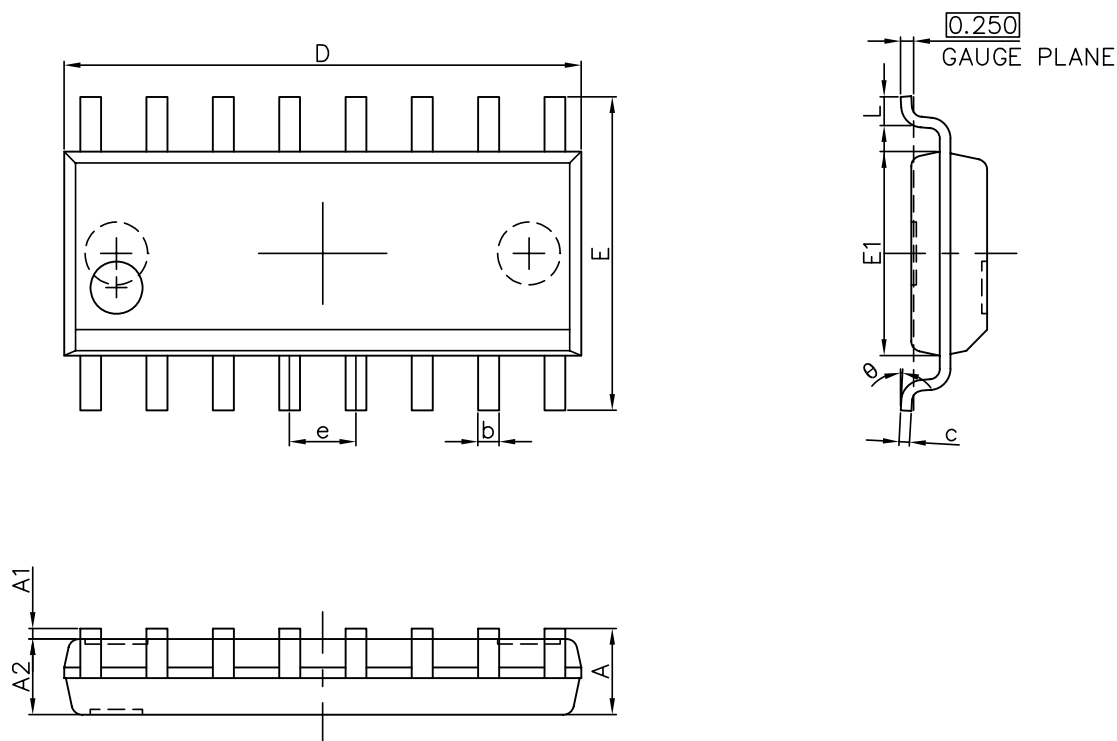
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Symbol	Parameter	Conditions	Min	Typ	Max	Unit
AC Electronics Characteristics						
t_{ON}	turn-On Time	$V_A=1.5V, C_L=50pF, R_L=50\Omega$		470		ns
t_{OFF}	turn-Off Time	$V_A=1.5V, C_L=50pF, R_L=50\Omega$		120		ns
t_{BBM}	break-before-make time	$V_A=1.5V, C_L=50pF, R_L=50\Omega$		600		ns
BW	-3dB Bandwidth	$C_L=0pF$		800		MHz
OIRR	off isolation	$f=10\text{ KHz}$		-80		dB
		$f=240\text{ MHz}$		-31		dB
X_{TALK}	crosstalk	$f=10\text{ KHz}$		-82		dB
		$f=240\text{ MHz}$		-26		dB
THD	total harmonic distortion	$f=20\text{Hz to } 20\text{KHz}$ $V_A=600\text{mVpp}, R_L=32\Omega$		-80		dB
Capacitance						
C_{ON}	On Capacitance	$f=100\text{KHz}$		12		pF
C_{OFF}	Off Capacitance	$f=100\text{KHz}$		4		pF

11. Package Outline

SOP-16L

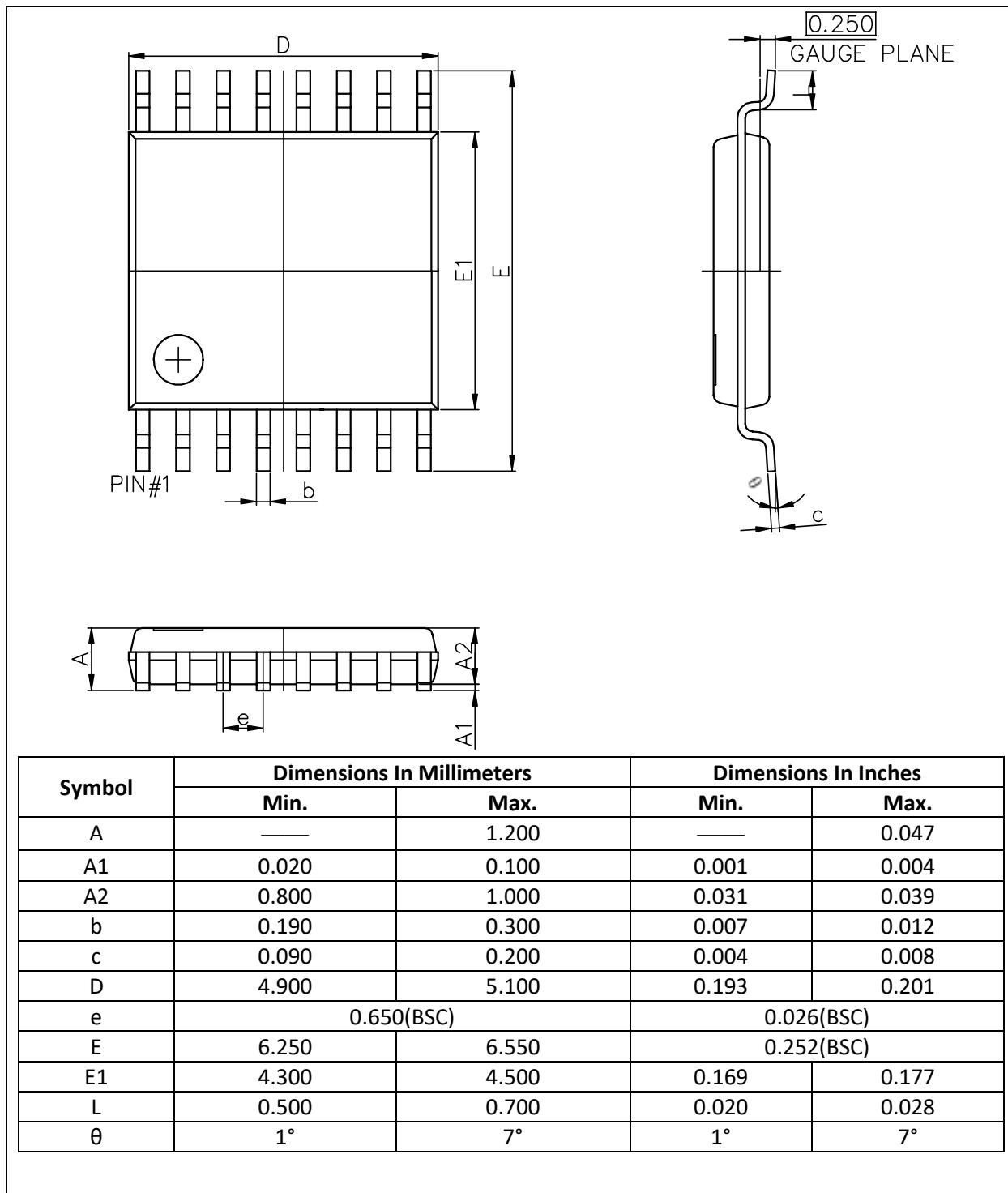


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	—	1.750	—	0.069
A1	0.150	0.250	0.006	0.010
A2	1.400	1.500	0.055	0.059
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.007	0.010
D	9.800	10.000	0.386	0.394
e	1.270(BSC)		0.050(BSC)	
E	5.900	6.100	0.232	0.240
E1	3.800	4.000	0.150	0.157
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°

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TSSOP-16L



12. Revision History

Table 7. Revision history

Document ID	Release Date	Data sheet status	Change notice	Supersedes
EM74CBT3257 Rev. 1.0	Jan 30, 2025	Product datasheet		